2.6.2. QIM	**Attainment of Programme outcomes and course outcomes are evaluated by the institution.**

Question paper

T.J.S. ENGINEERING COLLEGE

TJS Nagar, Kavaraipettai, Chennai 601206

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MID TERM I QUESTION PAPER

2020-2021 ODD SEMESTER

SUBJECT NAME	: EMBEDDED & REAL TIME SYSTEM	SUBJECT CODE	: EC8791
BRANCH	: ECE	YEAR/SEMESTER	: IV/VII
DATE	: 16/10/2020	TIME	: 3.00-4.30 PM
FACULTY NAME	: G.BHAVANI	MAX MARKS	: 50

CO 1	Outline the concepts of embedded systems
CO2	Describe the architecture and programming of ARM processor
CO3	Explain the basic concepts of real time operating system design
CO4	Model real-time applications using embedded-system concepts
CO5	Understand the concepts of embedded system design and analysis
CO6	Learn the architecture and programming of ARM processor

K1- Remembering
K2- Understanding
K3- Applying
K4- Analyzing
K5- Evaluating
K6- Creating

Answer ALL questions

PART A - (5 × 2 = 10 Marks)

Q.NO	QUESTION	MARK	CO-MAPPING	BLOOM'S TAXONOMY
1	What is the role of microprocessor in embedded computing?	2	CO1	K1
2	Design formalism for system design.	2	CO1	K1
3	Define quality assurance.	2	CO1	K2
4	Define program counter.	2	CO2	K1
5	What are the instruction sets is arm processor	2	CO2	K1

PART B –(2x 13=26 marks)

6a.	Explain in detail the design methodologies and design flow.	13	CO2	K2
	(OR)			
6b.	Explain in detail the design steps of modern train controller with suitable diagrams.	13	CO2	K1
7a.	Explain the architecture of ARMCORTEX M3/M4 processor	13	CO1	K4
	OR			
7b.	List out the features of LPC 214x family.	13	CO1	K2
	PART C (1x14=14 marks)			
8a.	Explain the design with computing the platform in embedded systems design.	14	CO2	K3
	OR			
8b.	Explain the architecture of ARM7TDMI-S processor	14	CO1	K1

FACULTY IN CHARGE

HOD-ECE

PRINCIPAL

T.J.S. ENGINEERING COLLEGE T.J.S.E.C. PERUVOYAL, KAVARAIPETTAI

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