



T.J.S ENGINEERING COLLEGE

Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai.

Accredited by NAAC / ISO 9001:2015 Certified Institution

TJS Nagar, Peruvoyal, Near Kavaraipettai, Gummidipoondi Taluk, Thiruvallur District -601206



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| 2.5.1. QIM | Mechanism of internal assessment is transparent and robust in terms of frequency and mode |
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Circular regarding internal assessment



T.J.S. ENGINEERING COLLEGE

D/S Nagar, Kavaraipettai, Chennai 601206



INTERNAL EXAM CELL

CIRCULAR FOR COMMON ASSESSMENT TEST -II (II & III YEAR)

Date: 02/05/2022

The Common Assessment Test-II Examinations for II & III Year B.E commencing from 05-05-2022 to 12-05-2022. The Exam timing will be from 09.00 A.M to 10.30 A.M.

1. The Time Table & seating arrangements for the exam will be displayed in the main notice board.
2. The Common Assessment Test -II will be conducted from 05-05-2022 to 12-05-2022
3. The faculty in charge handling the subjects are requested to submit the Hard & Soft copy of the question papers on or before 04-05-2022 through the HOD in ISO format to the mail ID examcell@tjsec.in through HOD's mail ID.
4. Graph/Log sheets if necessary for the subjects concerned has to be requested through letter from undersigned well before the date of conduct of examination.
5. The invigilators are requested to strictly instruct the students to be inside the examination hall at 10:30 AM. On genuine reasons late comers may be permitted to write the exam. Others will be viewed seriously and will not be permitted to write the examination.
6. The Faculty members who invigilate the hall are insisted to check the Codes / Materials permitted to use inside the examination halls.
7. It is the duty of the examiner to collect the papers in ascending order and return the same in the exam cell. The papers which are missing lies solely with the hall examiner and he/she are answerable for the same.
8. The faculty members receiving the answer scripts are requested to count the answer sheets in the exam cell itself and contact the coordinator in case of discrepancies
9. The faculty members are instructed to submit the individual subject mark sheets within 2 days from the conduct of the exam
10. CAT-II Syllabus: Unit-II Half Unit and Unit-III fully.

| DEPT | CIVIL | CSE | EEE | ECE | MECH | S&II |
|-------|-------|-----|-----|-----|------|------|
| H.O.D | | | | | | |

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T.J.S. ENGINEERING COLLEGE
Puduvoyal, Kavaraipettai,
Chennai - 601206.

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1. The Director
 2. The Principal
 3. Administrative officer
 4. All The H.O.D'S
 5. Library



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T.J.S. ENGINEERING COLLEGE

T.J.S. NAGAR, PERUVOYAL, NEAR KAVARAIPETTAI, CHENNAI-601106.



COMMON ASSESSMENT TEST-II TIME TABLE

| S.NO | DEPARTMENT | YEAR | 05.05.2022 | 06.05.2022 | 09.05.2022 | 10.05.2022 | 11.05.2022 | 12.05.2022 |
|------|------------|------|---------------------------------------|--|--|--|---|--|
| | | | SUBJECT CODE-SUBJECT NAME | SUBJECT CODE-SUBJECT NAME | SUBJECT CODE-SUBJECT NAME | SUBJECT CODE-SUBJECT NAME | SUBJECT CODE-SUBJECT NAME | SUBJECT CODE-SUBJECT NAME |
| 1 | CIVIL | II | CE8403-Applied Hydraulics Engineering | CE8404-Concrete Technology | MA8491-Numerical Methods | CE8401-Construction Technology and practices | CE8491-Soil Mechanics | CE8472-Strength of Materials II |
| 2 | CSE | II | CS8491/COMPUTER ARCHITECTURE | CS8492/DATABASE MANAGEMENT SYSTEM | CS8451/DESIGN AND ANALYSIS OF ALGORITHM | CS8450/SOFTWARE ENGINEERING | MA8402/PROBABILITY AND QUEUING THEORY | CS8493/OPERATING SYSTEM |
| | | III | CS8651/INTERNET PROGRAMMING | CS8603/DISTRIBUTED SYSTEMS | IT8076/SOFTWARE TESTING | CS8691/ARTIFICIAL INTELLIGENCE | CS8601/MOBILE COMPUTING | CS8602/COMPILER DESIGN |
| 3 | EEE | II | EES401-Electrical Machines - II | EES403-Measurements and Instrumentation | MA8491-Numerical Methods | EE8451-Control Systems | EES402-Transistors and Diodes | EES451-Linear Integrated Circuits and Applications |
| | | III | EE8002-Design of Electrical Apparatus | EE8005-Special Electrical Machines | EE8691-Embedded Systems | EE8601-Solid State Drives | EE8602-Protection and Switchgear | - |
| 4 | ECE | II | EC8452 Electronic Circuits II | GES291 Environmental Science and Engineering | MA8451 Probability and Random Processes | EC8451 Electromagnetic Fields | EC8491 Communication Theory | EC8452 Linear Integrated Circuits |
| | | III | MG8591 Principles of Management | EC8695 VLSI Design | EC8651 Transmission Lines and RF Systems | EC8691 Microprocessors and Microcontrollers | EC8652 Wireless Communication | EC8604 Wireless Networks |
| 5 | MECH | II | ME8492 KINEMATICS OF MACHINERY | MA8452 STATISTICAL AND NUMERICAL METHODS | ME8451 MANUFACTURING TECHNOLOGY - II | ME8491 ENGINEERING METALLURGY | CE8495 STRENGTH OF MATERIALS FOR MECHANICAL ENGINEERS | ME8492 THERMAL ENGINEERING - I |
| | | III | ME8692 FINITE ELEMENT ANALYSIS | ME8694 HYDRAULICS AND PNEUMATICS | ME8691 COMPUTER AIDED DESIGN AND MANUFACTURING | ME8693 HEAT AND MASS TRANSFER | ME8651 DESIGN OF TRANSMISSION SYSTEMS | ME8691 AUTOMOBILE ENGINEERING |

Copy to,

| CIVIL | CSE | EEE | ECE | MECH | SAH |
|--------------------|--------------------|--------------------|--------------------|--------------------|--------------------|
| <i>[Signature]</i> | <i>[Signature]</i> | <i>[Signature]</i> | <i>[Signature]</i> | <i>[Signature]</i> | <i>[Signature]</i> |

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Peruvoyal, Kavarai Pettai,
Chennai-601106, Dist - 601 206.

T.J.S. ENGINEERING COLLEGE

TJS Nagar, Kavaraipettai, Chennai 601206



INTERNAL EXAM CELL

Date: 26/05/2022

CIRCULAR FOR COMMON ASSESSMENT TEST - III (I YEAR)

The Common Assessment Test-III Examinations for I Year B.E commencing from 01-06-2022 to 08-06-2022. The Exam timing will be from 9.00 A.M to 10.30 A.M.

1. The Time Table & seating arrangements for the exam will be displayed in the main notice board.
2. The Common Assessment Test -III will be conducted from 01-06-2022 to 08-06-2022
3. The faculty in charge handling the subjects are requested to submit the Hard & Soft copy of the question papers on or before 31-05-2022 through the HOD in ISO format to the mail ID examcell@tjsec.in through HOD's mail ID.
4. Graph/Log sheets if necessary for the subjects concerned has to be requested through letter from undersigned well before the date of conduct of examination.
5. The invigilators are requested to strictly instruct the students to be inside the examination hall at 08:50 AM. On genuine reasons late comers may be permitted to write the exam. Others will be viewed seriously and will not be permitted to write the examination.
6. The Faculty members who invigilate the hall are insisted to check the Codes / Materials permitted to use inside the examination halls.
7. It is the duty of the examiner to collect the papers in ascending order and return the same in the exam cell. The papers which are missing lies solely with the hall examiner and he/she are answerable for the same.
8. The faculty members receiving the answer scripts are requested to count the answer sheets in the exam cell itself and contact the coordinator in case of discrepancies
9. The faculty members are instructed to submit the individual subject mark sheets within 2 days from the conduct of the exam
10. CAT-III Syllabus: Unit- IV fully and Unit - V Half Unit.

| DEPT | CIVIL | CSE | EEE | ECE | MECH | S&H |
|-------|-------|-----|-----|-----|------|-----|
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EXAMCELL CO-ORDINAOR

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